

Organizing Committee

- General Chair
 - John Lockwood (Washington University in St. Louis)
- Program Chair
 - Mark Johnson (Purdue University)
- Poster Chair
 - Wayne Burleson (University of Massachusetts)
- Proceedings Chair
 - Andrzej Rucinski (University of New Hampshire)
- Steering Committee:
 - Jim Aylor (University of Virginia)
 - Don Bouldin (University of Tennessee)
 - Bob Klenke (Virginia Commonwealth University)

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Program Committee

James Aylor, Univ. of Virginia	Alex Jones, Univ. of Pittsburgh
Kunihiro Asada, Univ. of Tokyo, Japan	Mark C. Johnson, Purdue Univ.
Juergen Becker, Univ. Karlsruhe, Germany	Srinivas Katkoori, USF-Tampa
Yves Bertrand, LIRMM, France	Robert Klenke, Virginia Commonwealth Univ.
Don Bouldin, Univ. of Tennessee	David L. Landis, Pittsburgh Digital Greenhouse
Rich Brown, Univ. of Utah	Wallace Leigh, Alfred Univ.
Wayne Burleson, Univ. of Massachusetts	Steven Levitan, Univ. of Pittsburgh
Joseph Cavallaro, Rice Univ.	John Lockwood, Washington Univ. in St. Louis
Mark Chang, Olin College, MA	Peter Marwedel, Univ. of Dortmund, Germany
Morris Chang, Iowa State Univ.	Aleksandar Milenkovic, Univ. of Alabama in Huntsville
Naehyuck Chang, Seoul National Univ., Korea	John Nestor, Lafayette College
Minsu Choi, Univ. of Missouri	Peter Nilsson, Lund Univ., Sweden
Bernard Courtois, TIMA-CMP, France	David Parent, San Jose State Univ.
Carl Das, IMEC, Belgium	Volnei Pedroni, Federal Center of Tech. Educ., Brazil
Koushik Das, IBM Watson Research Center, NY	Greg Peterson, Univ. of Tennessee
Michel Declercq, Swiss Federal Inst. of Tech. (EPFL)	Xu Yong Ping, National Univ. of Singapore
Qu Gang, Univ. of Maryland	Hardy J. Pottinger, Univ. Missouri - Rolla
Manfred Glesner, Tech. Univ. Darmstadt, Germany	Ricardo Reis, UFRGS, Brazil
James Hamblen, Georgia Tech	Marta Rencz, Tech. Univ. of Budapest, Hungary
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Tina Hudson, Rose Hulman	Murali Varanasi, Univ. of North Texas
Toru Ishihara, Fujitsu Labs of America	Miroslav Velez, Reservoir Labs, NY
Eric Johnson, Valparaiso Univ.	

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Sunday Morning, June 12, 2003

- Registration and Continental Breakfast
 - 8:00-8:30 am

- Session 1 : Keynote & Opening (John Lockwood)
 - Presentations : 8:30am - 10:00am
 - Break : 10:00am – 10:30am

- Session 2 : Posters (Wayne Burleson)
 - Presentations : 10:30am – Noon
 - Lunch : Noon – 1:30p

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Sunday Afternoon, June 12, 2003

- Session 3 : System Level Design (Don Bouldin)
 - Presentations : 1:30 - 3:00 pm
 - Break : 3:00pm - 3:30pm

- Session 4 : Posters (Wayne Burleson)
 - Presentations : 3:30pm – 5:00pm
 - Mixer : 5:00pm - 5:45pm
 - Dinner : 5:45pm – 6:45pm
 - Evening Keynote : 6:45pm – 7:15pm

- Session 5 : EDA Infrastructure Panel (Mark Johnson)
 - Discussion : 7:30pm – 9:00pm

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Monday Morning, June 13, 2001

- Continental Breakfast
 - 8:00am - 8:30am

- Session 6 - Novel Curricula (John Nestor)
 - Presentations : 8:30am – 9:50am
 - Break : 9:50am – 10:20am

- Session 7 - Design, Test, & Verification (Tina Hudson)
 - Presentations : 10:20a – Noon

- DAC Exhibits
 - Open to all : Noon - 6:00pm

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Upcoming Conferences

- 2006:
- European Workshop on Microelectronics Education
- 8-9 June 2006 in Stockholm; hannu@ele.kth.se

- Design Automation Conference
- 24-28 July 2006 in San Francisco; www.dac.com

- 2007:
- Microelectronic Systems Education Conference
- 3-4 June 2007 in San Diego; www.mseconference.org
- Co-located with DAC: 4-8 June 2007

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Opening Session

8:30am - 8:50am : Sunday, June 12, 2003

Message from the General Chair

John W. Lockwood

Message from the Program Chair

Mark Johnson

Message from the Poster Chair

Wayne Burleson

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Session 2: Curriculum

8:50am – 10:00am : Sunday, June 1, 2003

Introduction : John W. Lockwood

- **Keynote : Ralph Cavin**
 - Semiconductor Research Corporation (SRC)
- **Johannes Grad, James E. Stine, David D. Neiman**
 - Real World SOC Experience for the Classroom

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Ralph Cavin (1/2)

- Ph.D. E.E., 1968, Auburn University
- 1962-1965, Senior Engineer at Martin-Marietta (Orlando, Florida)
- 1968-1983, Joined Texas A&M, reached Full Professor and Assistant Head of EE for Research
- 1983-1989, Semiconductor Research Corporation, Director of Design Sciences research
- 1989-1995, Head of ECE, then Dean of Engineering, North Carolina State Univ.
- Present: Vice President for Research Operations at the Semiconductor Research Corporation

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Ralph Cavin (2/2)

- Technical interests:
 - VLSI circuit design
 - computer-aided design of microelectronic systems
 - control theory applied to semiconductor manufacturing
 - applications of computing and telecommunications to engineering education
- Fellow of the Institute for Electrical and Electronics Engineers
- Member of the Board of Directors of the International Engineering Consortium and the IEEE Computer Advisory Board
- Author or co-author of over 100 refereed papers

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Johannes Grad

- Ph.D. Candidate at Illinois Institute of Technology
 - VLSI Computer Architecture
 - CAD Laboratory Research Group
- BEE (German Dipl. Ing. cand.), 1999, University of Erlangen - Germany
- Intern with Cadence Design Systems
- Research:
 - high speed binary addition
 - design flows for digital systems
 - has created a suite of standard cell design kits for MOSIS supported processes