

OVERCOMING THE LIMITATIONS OF TRADITIONAL MEDIA FOR TEACHING MODERN PROCESSOR DESIGN

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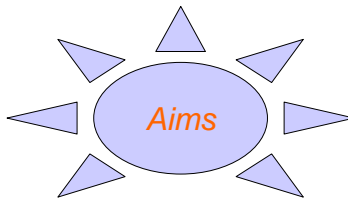
Visualization of the Dynamic Behavior of Modern Processors

Using Multi Media Techniques

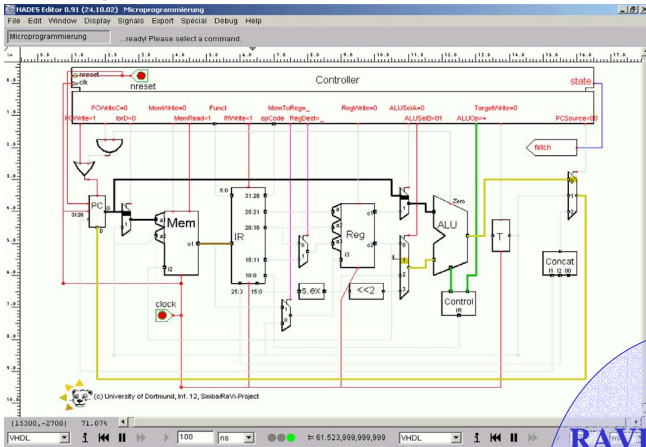
Motivating students to study processor architectures

Providing of Interactiveness

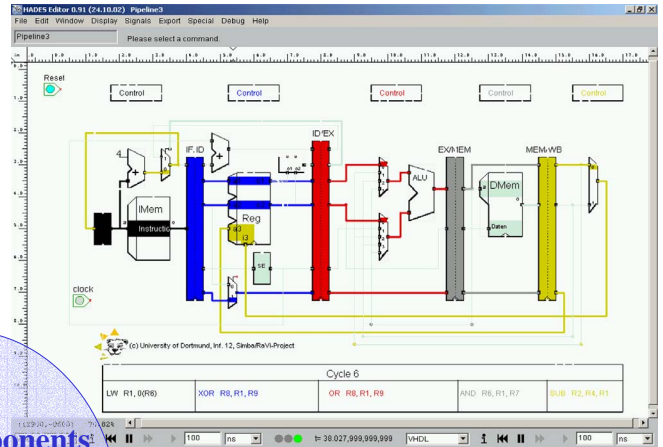
Reusable and Upgradeable Learning Components



Microprogrammed MISP Processor

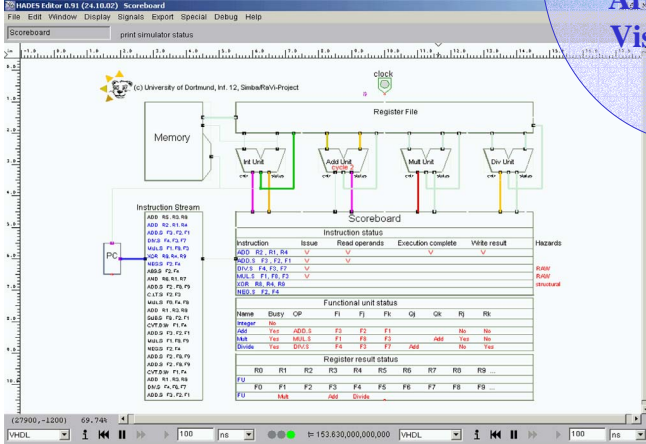


The Datapath of a MISP Pipeline

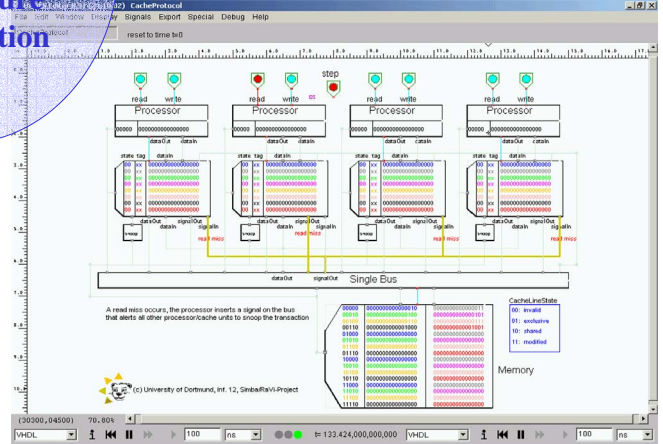


RAVi Components for Computer Architecture Visualization

Dynamic Instruction Scheduling by Score-boarding



Cache Operations of a Multi Processor Architecture



RaVi Homepage: is12-www.cs.uni-dortmund.de/ravi



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