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**2003 International Conference on
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Murali Varanasi, USF - Tampa

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Sunday Morning, June 1, 2003

- Registration and Continental Breakfast
 - 8:00-10:00 am

- Session 1 : Keynote & Opening Session
 - Session 1 : 10:00am - 11:30am

- Posters & Buffet Lunch
 - Session 1P : 11:30-1:30 pm

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Sunday Afternoon, June 1, 2003

- Curriculum
 - Session 2 : 1:30-3:00 pm

 - Break : 3:00-3:30pm

- EDA Infrastructure Panel
 - Session 3 : 3:30-5:30 pm

- Posters & Buffet Dinner
 - Session 2P: 6:00-8:00 pm

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Monday, June 18, 2001

- Continental Breakfast
 - 8:00-9:00 am

- Nanoscience and Government Infrastructure
 - Session 4 : 9:00-10:30 am
 - Break : 10:30-11:00 am

- System-On-Chip (SoC)
 - Session 5 : 11:00-12:30 pm

- DAC Exhibits
 - Open to all : 12:30p-6:00pm

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Session 1: Opening Session

10:00am - 11:30am : Sunday, June 1, 2003

Message from the General Chair

Bob Klenke, Virginia Commonwealth University.

- **Keynote**
 - Peter Kogge (Assoc. Dean, University of Notre Dame)

- **Computing Curricula-Computer Engineering**
 - Murali Varanasi (presented by Jim Aylor)

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Session 2: Curriculum

1:30 - 3:00 pm : Sunday, June 1, 2003

Introduction : John Lockwood

- **Teaching Trade-offs in System-level Design Methodologies**
 - Kazuo Sakiyama, Patrick Schaumont, David Hwang, Ingrid Verbauwhede
- **VALID: Custom ASIC Verification and FPGA Education Platform:**
 - Patrick Murphy, J. Patrick Frantz, Erik Welsh, Ricky Hardy, Tinoosh Mohsenin and Joseph Cavallaro
- **A New Hardware/Software Co-design Environment and Senior Capstone Design Project for Computer Engineering**
 - Robert H. Klenke, Jerry H. Tucker, Jason M. Blevins
- **Teaching Bus Architectures with a Basic, Hands-on SOC Platform:**
 - Marc Bertola, Guy Bois

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Session 4 : Nanoscience & Government Infrastructure

9:00 - 10:30 am : Monday, June 2, 2003

Introduction: Jim Aylor

- **Future Direction in Microelectronics: Nanotechnology**
 - James Murday, Naval Research Laboratory
- **Opportunities for Educational Reform at NSF**
 - Vasundara Varadan, Division Director for Electrical and Communications Systems, National Science Foundation
- **Paradigm of Design for Biosystem-on-a-chip (BioSoC)**
 - Robert Ewing and Hoda Abdel-Aty-Zohdy
- **Simplifying Analog CMOS Design for Undergraduates**
 - Wallace Leigh

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Session 5 : System On Chip (SoC)

Monday, June 2, 2003 - 11:00-12:30 pm

Introduction: Don Bouldin, Univ. of Tennessee

- **Exploiting SoC for VLSI Education**
 - Wayne Burleson, Univ. of Massachusetts
- **Industry Needs and Expectations of SoC Design Education**
 - Grant Martin, Cadence Design Systems
- **System-On-Chip Internet Firewall**
 - John Lockwood, Washington Univ. St. Louis
- **CMC, CMP and MOSIS -- The Scale of Cooperation Increases as the Dimensions of Microchips Decrease**
 - Bernard Courtois, CMP

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Extra Proceedings

- Extra Microelectronic Systems Education Proceedings Available
 - MSE'99 [Sold Out]
 - MSE'01
 - MSE'03

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